

## **GENERAL DESCRIPTION**

The CM3708A is a low-noise, pulse-width-modulated (PWM), DC-DC step-down converter. It powers logic and transmitters in small wireless systems such as cellular phones, communicating PDAs, and handy-terminals. The device features an internal synchronous rectifier for high efficiency; it requires no external Schottky diode. Excellent noise characteristics and fixed-frequency operation provide easy post-filtering. The CM3708A is ideally suited for Li-lon battery applications. It is also useful for +3V or +5V fixed input applications.

The device operates in one of four modes. Forced PWM mode operates at a fixed frequency regardless of the load. Shutdown mode places the device in standby, reducing quiescent supply current to under 0.1µA.

The CM3708A can deliver over 3.0A. The output voltage can be adjusted from VREF to VIN. The input range is from 2.0V to 5.5V. Other features of the CM3708A include high efficiency, low dropout voltage. It is available in a space-saving 16-pin SOP & TSSOP package.

## **FEATURES**

- Patent Filed #6,452,366
- ♦ 600KHz switching and synchronization
- ◆ Dynamic output-voltage adjustment from VREF to VIN
- 3A Guaranteed Output Current
- ♦ 95% Efficiency
- No Schottky Diode Required
- ♦ 16-pin PSOP/PTSSOP power packages
- ♦ 6A Low Noise Current Limit Protection
- External Soft Start
- ◆ Rail to Rail output Buck Converter

## **APPLICATIONS**

- Cellular Phone
- ◆ Cordless Phone
- ♦ PDAs and Handy-Terminals
- ♦ AGP Chipset Supplies

- ◆ CPU I/O Supplies
- Notebook Chipset Supplies
- Battery Operated Devices

### PIN CONFIGURATION

PSOP-16 (PS16) / PTSSOP-16 (PT16) Top View

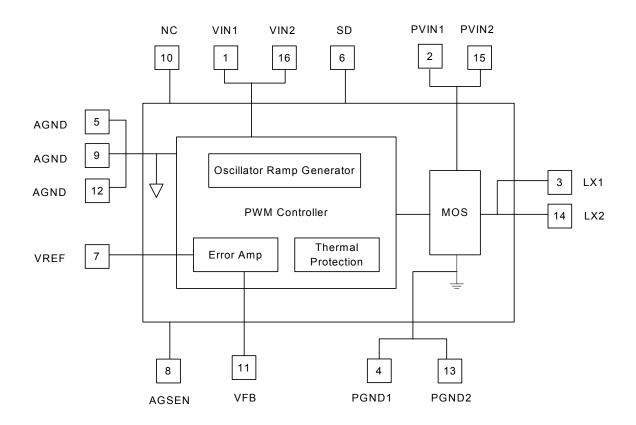
			1	
1	VIN1	VIN2		16
2	PVIN1	PVIN2		15
3	LX1	LX2		14
4	PGND1	PGND2		13
5	AGND	AGND		12
6	SD	VFB		11
7	VREF	NC		10
8	AGSEN	AGND		9



## **PIN DESCRIPTION**

Pin No.	Symbol	Description	Operating Rating				
PIII NO.	Syllibol	Description	Min.	Тур.	Max.	Unit	
1,16	VIN1, VIN2	Voltage supply for internal circuits		2.5	5.5	V	
2,15	PVIN1, PVIN2	Voltage supply for output power transistors	2	2.5	5.5	V	
3,14	LX1, LX2	Inductor connection to the Drains of the internal power MOSFETs			5	V	
4,13	PGND1, PGND2	Ground for output power transistors					
5,9,12	AGND	Ground for internal reference voltage divider					
8	AGSEN	Ground for remote sensing					
6	SD	Shutdown active high. CMOS input level	0.75 x		VIN +	V	
			VIN		0.3V		
7	VREF	V <sub>OUT</sub> Set Voltage	0		VIN	V	
10	NC	No Connection					
11	VFB	Feedback node for the V <sub>OUT</sub>			5	V	

## **BLOCK DIAGRAM**





## ORDERING INFORMATION

Part Number	Temperature Range	Package
CM3708AIT	-40℃ to 85℃	16-Pin PTSSOP (PT16)
CM3708AGIT*	-40℃ to 85℃	16-Pin PTSSOP (PT16)
CM3708AIS	-40°C to 85°C	16-Pin PSOP (PS16)
CM3708AGIS*	-40℃ to 85℃	16-Pin PSOP (PS16)

<sup>\*</sup>Note: G : Suffix for Pb Free Product

## **ABSOLUTE MAXIMUM RATINGS**

Absolute maximum ratings are those values beyond which the						
device could be permanently damaged.						
PVIN/VIN0.3V to 6.0V						
Voltage on Any Other Pin GND – 0.3V to VIN + 0.3V						
Output Current, Source or Sink 3.0A						

Absolute maximum ratings are those values beyond which the

Junction Temperature	150°C
Storage Temperature	65°C to 125°C
Lead Temperature (Soldering, 5 sec)	260°C
Thermal Dissipation ( $\theta$ JC )	50°C/W

## **OPERATING CONDITIONS**

Temperature Range ......-40°C to 85°C PVIN Operating Range ......2.0V to 5.5V

**ELECTRICAL CHARACTERISTICS** (Unless otherwise stated, these specifications apply T<sub>A</sub>=25°C; VIN=+3.3V and PVIN=+3.3V) maximum ratings are stress ratings only and functional device operation is not implied. (Note 1)

0	Parameter.	T O	CM3708A			
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
SWITCHING	REGULATOR					
$V_{REF}$	Adjustable Output Voltage		0		VIN	V
fsw	Switching Frequency	CM3708A	480		660	KHz
I <sub>OUT(RMS)</sub>	Maximum Output RMS Current	CM3708A			3.0	Α
I <sub>OUT(PEAK)</sub>	Maximum Output Peak Current	CM3708A			6.0	Α
I <sub>limit</sub>	Current limit	CM3708A		6		Α
MOSFETs						
RDS <sub>(ON)</sub>	Drain to Source on-State Resistance	PVIN=5V		150	180	$\mathbf{m}\Omega$
SUPPLY						
	0:10	VFB = 1.4V		000		
$I_{VIN}$	Quiescent Current	LC unconnected		200		μA
I <sub>PVIN</sub>	Outline and Outline at	VFB = 1.4V		500		
	Quiescent Current	LC unconnected		500		μA

Note 1: Limits are guaranteed by 100% testing, sampling, or correlation with worst case test conditions

Note 2: VIN, PVIN = 3.3V ±10%

Note 3: It's not 100% test



## FUNCTIONAL DESCRIPTION

The CM3708A step-down, pulse-width-modulated (PWM), DC-DC converter has an adjustable output range from VREF improves efficiency and eliminates an external Schottky diode. pin, the output voltage follows the voltage at the VREF pin. Fixed-frequency operation enables easy post-filtering, thereby providing excellent noise characteristics. As a result, the OTHER SUPPLY VOLTAGES CM3708A is an ideal choice for many small wireless systems.

#### **VREF**

The reference voltage could be ranged from 0V to VIN.

#### **OUPUTS**

The output voltage pins (LX1, LX2) are tied to the RF power amp, via an external inductor. Output voltage is determined by the VREF inputs so it can be from 0V to VIN.

#### **Internal Power Switches:**

CM3708A has been integrated 2 Power MOSFETs whose RDS<sub>(ON)</sub> is around 100m Ohm each.

#### Low Noise Current Limit:

CM3708A's current limit is a low noise current limit. It increase the system loop noise immunity while it is doing the current limit protection for the system. The current limit is around 6A for the normal 3A operation.

#### **INPUTS**

The input voltage reference pin, VREF determine the output to the input voltage (VIN). An internal synchronous rectifier voltages (LX1, LX2). If a specific voltage is forced at the VREF

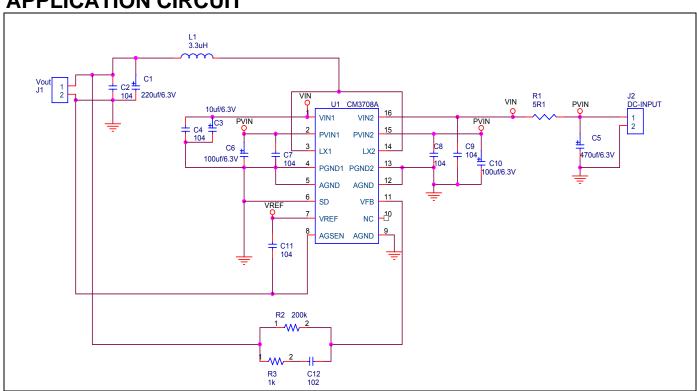
Several inputs are provided for the supply voltages: PVIN and VIN.

The PVIN provide the power supply to the power MOSFETs. VIN provides the voltage supply to the logic section and internal error amplifiers.

### **FEEDBACK**

The VFB pin is an input that can be used for closed loop compensation. This input is derived from the voltage output. AGND pin is a contact node of internal resistor divider for remote sense.

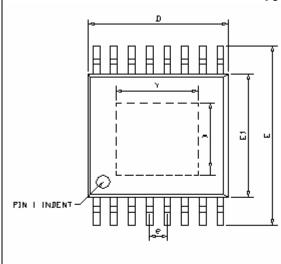
# **APPLICATION CIRCUIT**





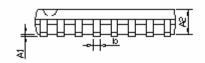
## **PACKAGE DIMENSION**





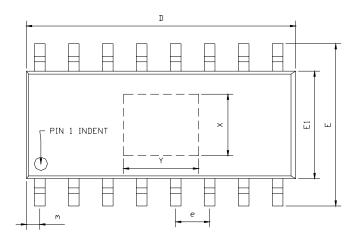
	DOMERNOUS IN MILLIMETERS			DOMENBICINS IN DACHB		
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
Αí	0.05		015	0.002		0.008
AR	D.84		0 94	D. <b>033</b>		0.037
ъ	0.20		0.80	0.008		3 10.0
¢	0.10		0.20	0.004		0.008
D	4.88		513	0.192		0 202
В	8.28		6.66	0.248		0.268
B1	4,20		4,50	0.189		0.177
e		0.86			0.028	
L	D.51		0.41	0.020		0.028
0	D*		ð	5		8

EXPOSED PAD DIMENSION : (mm) PAD SIZE: X=30; Y=30



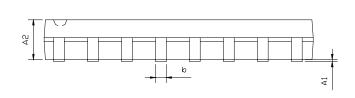


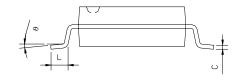
## **16-PIN PSOP (PS16)**



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHS			
SIMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A1	0.05		0.15	0.002		0.006	
A2	1.40		1.55	0.055		0.061	
b	0.30		0.51	0.012		0.020	
C	0.15		0.26	0.006		0.010	
D	9.80		10.06	0.386		0.396	
E	5.79		6.20	0.228		0.244	
E1	3.76		4.01	0.148		0.158	
e		1.27			0.050		
L	0.38		0.69	0.015		0.035	
m	0.43		0.69	0.017		0.027	
θ	0°		8°	0°		8°	

EXPOSED PAD DIMENSION : (mm) PAD SIZE: X=2.3 ; Y=2.8









#### **IMPORTANT NOTICE**

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